



PATENT

Docket No.: ACT-280COA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2116

Examiner: Chang, Eric

Serial No.: 10/722,636

Filed: November 25, 2003

In re Application of: Plants et al.

For: DELAY LOCKED LOOP FOR AN FPGA ARCHITECTURE

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 3/14/05.

Signed Krista Thompson

Krista Thompson

RESPONSE TO FINAL OFFICE ACTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to a final Office Action dated January 12, 2005, kindly amend the above-identified application as follows.